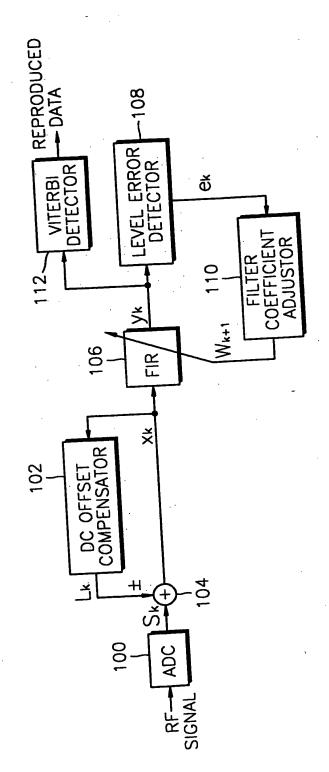
FIG. 1 (PRIOR ART)



REPRODUCED DATA -208 VITERBI LEVEL DECISION UNIT LEVEL ERROR DETECTOR VITERBI DETECTOR ά ADAPTIVE PROCESSOR × ž Wk+1 206 FIR, × DC OFFSET COMPENSATOR 202 204 + Š 200 ADC RF---SIGNAL

210

FIG. 2

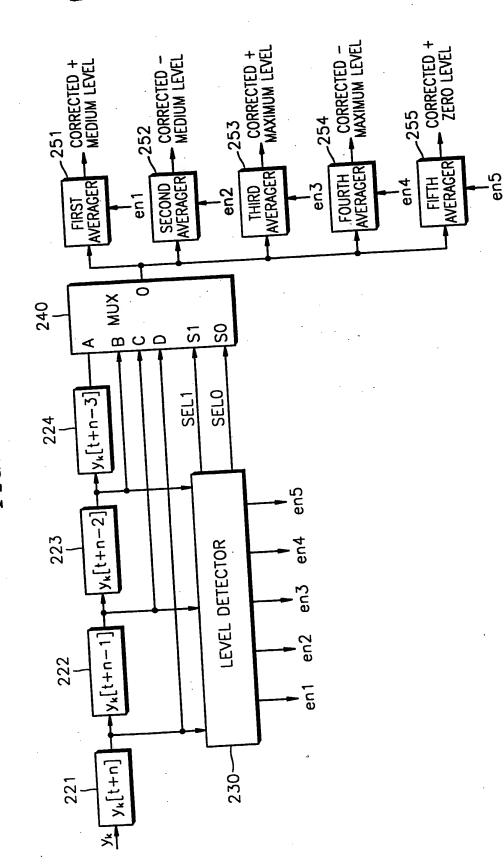


FIG. 3

FIG. 4

		CIM	+MAX	-MAX		Į.	0 110	OUTPUT	
SNOITIUNOS	LEVEL LEVEL		LEVEL	EVEL NABI F E		SEL!	SELU	OF MUX	
	ENABLE E	NABLE	INABLE ED	ISARI F	ISABLE	0	-	Jvk[t+n-1	
v.[t+n-1] * y.[t+n]<0 ENABLE DISABLE DISABLE DISABLE DISABLE	ENABLE DISABLE DISABLE DISABLE	SABLE	NOABLE PO	SABI FIL	ISABLE	0	0	$y_k[t+n]$	
AND yk[t+n-1]>0	DISABLE	NABLE	NOADLL D	SARI F	SABLE	0	0	yk[t+n]	
v.[t+n-1] * y.[t+n]<0 ENABLE DISABLE DISABLE DISABLE DISABLE	ENABLE DISABLE DISABLE DISABI FIDISABLE	SABLE	JISABLE D	SABI F	SABLE	0	-	$y_k[t+n-1]$	
AND yk[t+n-1]<0	DISABLE	NABLE	וואסרבור					7	
$y_{k}[t+n-2], y_{k}[t+n-1]$	DISABLE DISABLE ENABLE DISABLE	SABLE	ENABLE	SABLE	DISABLE	o	- .	7×1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-	7
AND %[LTI]									٦
y _k [t+n-2], y _k [t+n-1]	DISABLE DISABLE ENABLE DISABLE	DISABLE	DISABLE	ENABLE	DISABLE	0	<u></u>	yk[t+n-1]	
AND VALCTUITY									l
					i				

FIG. 5

					=		<u> </u>	T	$\neg \tau$		اد	<u>.</u> [Ţ	-	\	<u>, '</u>	1	<u>-</u>	١ ،	근	
TIOTI	OF MUX	$y_k[t+n-1]$	v.[t+n-3]		yk[t+n-3]	$y_k[t+n-1]$	$v_k[t+n-2]$		yk[t+n]	$y_k[t+n]$		Jk[[+11-4]	1	אר		yk[t+n-1] 		yk[t+n-1		y*[t+n]	
F	SELO	<u>×</u>	-	` -\	- >	-	c	١	0	0	1	0		,-		-	_	<u>-</u>	-	0	
-	SEL 1	0	\	-	0	-	-	-	0	0		-		0		0		0	1	0	
	ZERO LEVEL S	ABLE		SABLE	SABLE	SABLE	1 6	SABLE	SABLE	ISABI F	1000	SABLE		SABLE		DISABLE		ENABLE		ENABLE	
- 1	-MAX Z	ABI FINA	70-	ABLEDIS	ABLEDI	ABLEDI		SABLEIDI	SABLED	CAB! FIN	WOLL S	ISABLE 0	7.	ISABLE		NABLE		DISABLE		DISABLE	
.]	+MAX LEVEL LEVEL	ENABLE EIN	ABLEIOIS	ABLEDIS	ARI FIDIS	ABI FIDI		SABLEDI	SABLEDI		SABLED	SABLED	ig	NABLE D	+	SABLE		SABLE		SABLE	
ļ	-MID +N	BLE EN	ABLE UIS	BLE DIS	SICI ZI IGV	70 C	ABLE DI	ABLE DI	ARI F DI		SABLEIDI	ABLE D	+	SABLEE		SABLED		SABLE		SABLE	
	+MID -N	ENABLE ENABLE	ENABLE DISABLE	DISABLE ENABLE DISABLE DISABLE	CONTRACT OF SABIFORSABLE DISABLE	ENABLE DISABLE DISABLEDISABLE	ABLE EN	FNARI F DISABLE DISABLE DISABLE	PISABLE DISABLEDISABLE DISABLE	WOLL L.	ENABLE DISABLE DISABLE DISABLE DISABLE	DISABLE DISABLE DISABLE DISABLE		DISABLE DISABLE ENABLE DISABLE DISABLE		DISABLE DISABLE ENABLE DISABLE	2000	ICARI FI	יייייייייייייייייייייייייייייייייייייי	SABLE	
	HMID LEVEL							_									<u>۔۔۔</u> ع	0,		S ₁	-+-
	SINCIFIC	CONDITIONS	$\sqrt{(t+n-2)^2}$ $\sqrt{(t+n-1)} \le 0$	$ y_{k}[t+n-2] \le y_{k}[t+n-1] $	AND yk[t+n-1] >0	y*[t+n-2]* y*[t+n-1] NO; - [t,] _ [] [t+n-1] NO;	AND v.[t+n-1] <0	11<0, -11<0,	$y_{k}[t+n-2]$ $y_{k}[t]$ $y_{k}[t+n-1]$	AND yk[t+n-2] >0]* v.[t+n-1] <	$ v_{k}[t+n-2] > v_{k}[t+n-1] $	AND yk[t+n-2] ≤0	yk[t+n-2], yk[t+n-1]	AND VELTHAN TH	$y_{k}[t+n-2], y_{k}[t+n-1]$	AND yk[t+n]	Vr[t+n-1] * Vr[t+n]≤0 DISABLE DISABLE DISABLE ENABLE	+n-1] ≤ yk[t	y*[t+n-1] * y*[t+n] <0 DISABLE DISABLE DISABLE DISABLE	t+n-1] > yk[t
		CON	v _k [t+n-2]	$\int_{y_k} [t+n-2]$	AND Y	$y_k[t+n-2]$	IYKLITTI AND VI		yk[t+n-z lv [t+n-2	AND y	1, [++n-7		AND >	y _k [t+n	A DI	×, (t+r)	- 10	Vr[t+n	ANDIVE	v _k [t+n	AND YE

FIG. 6

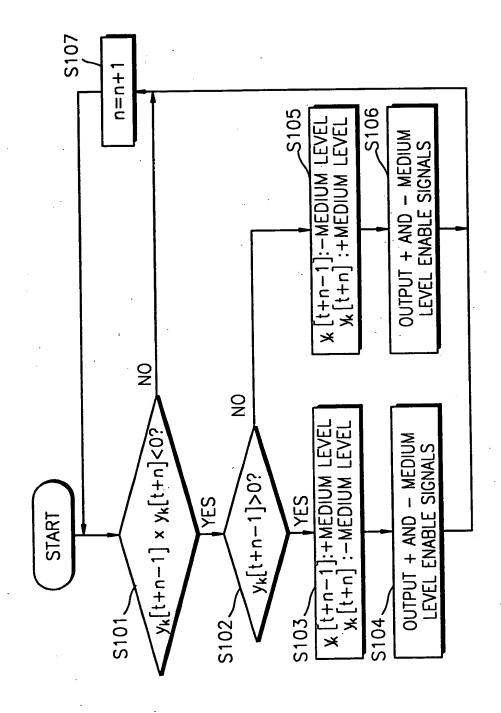


FIG. 7

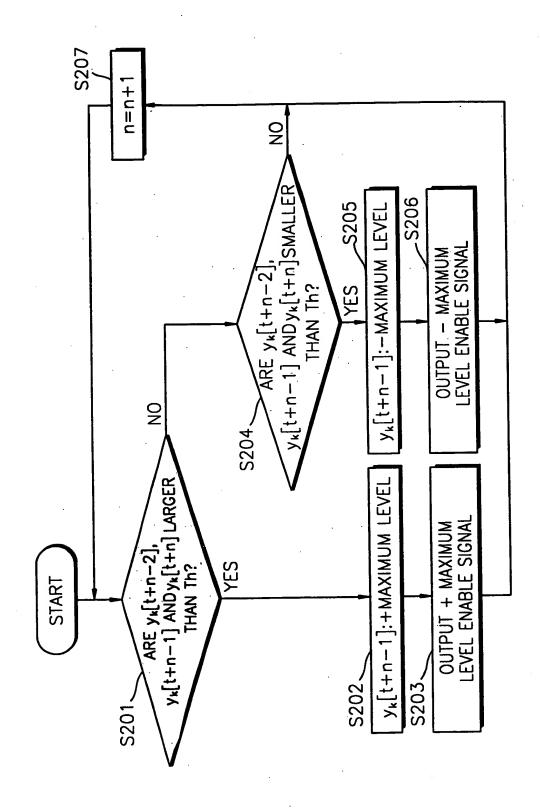


FIG. 8

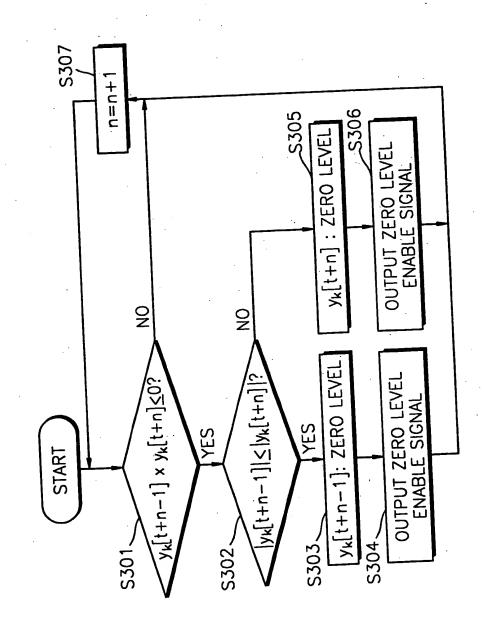


FIG. 9

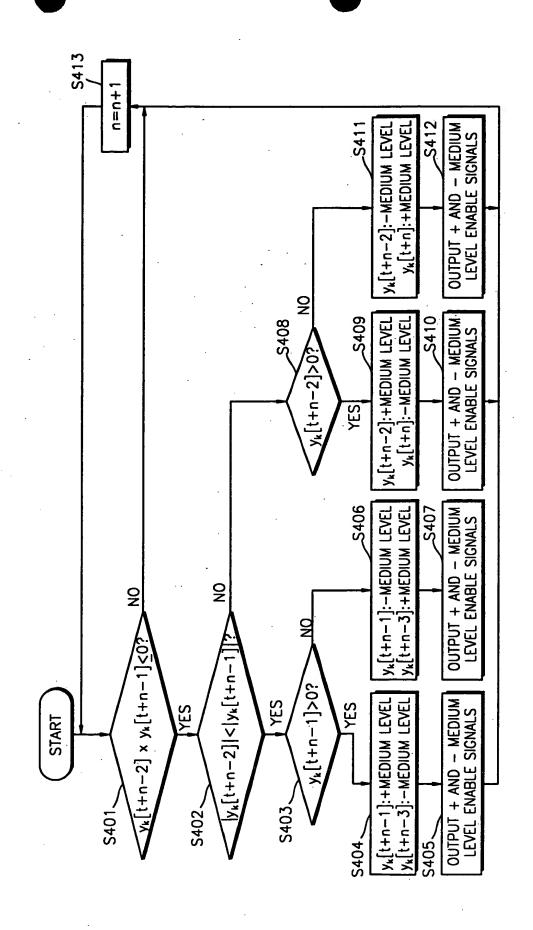


FIG. 10

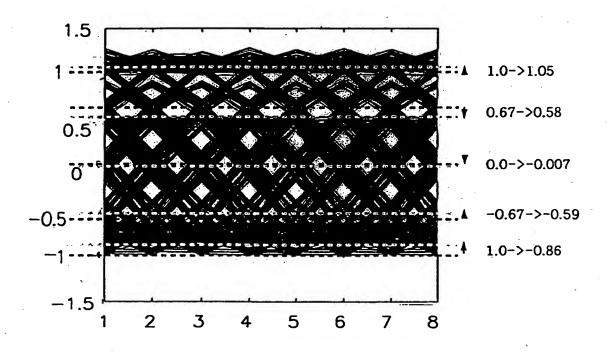


FIG. 11

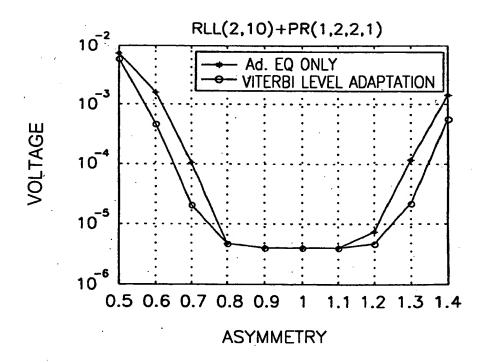


FIG. 12

